



BAPUJI EDUCATIONAL ASSOCIATION (Regd)  
BAPUJI INSTITUTE OF ENGINEERING AND TECHNOLOGY, DAVANGERE  
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

CLASS TIME TABLE FOR ODD SEMESTER 2022-23

SEMESTER III 'B'

Room No - A 254 / A 252

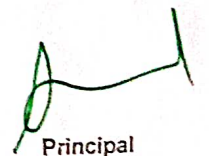
DAY/TIME	8-9am	9-10am	10.30-11.30am	11-30-12.30pm	2-3 pm	3-4 pm	4-5 pm
MON	21EC34 SWS	21EC33 Dr. KGH	21EC32 SBS		21EC32 B3 (SBS+SKG) 21ECL35 B1 (MC+Dr.KGH) 21EC384 B2 (PGN+Dr.AKG) 1 p.m. to 3 p.m.	21KSK37 (Aryabhata Seminar Hall) 21KBK37 (TX307)	
TUE		21UH36 SBS			21MATDIP31 SMC	21EC33 R Dr. KGH	21EC34 R SWS
WED	21EC32 B1 (SBS+RPN) BS 21EC33 B2 (Dr.KGH+Dr.AKG) 21ECL35 B3 (MC+Dr.KMP)		21EC34 SWS	21EC32R SBS	21MATDIP31 OGD	21MAT31 PRJ	21EC33 Dr. KGH
THU	21MAT31 PRJ	21EC32 SBS			21MATDIP31 OGD		
FRI	21MATDIP31 SMC	21EC33 Dr. KGH	21MAT31 PRJ	21EC34 SWS		21EC33 B1 (Dr.KGH+Dr.GK) 21ECL35 B2 (MC+SWS+Dr.GSS) 21EC384 B3 (PGN+RPN)	
SAT	21EC32 B2 (SBS+VHV) 21EC33 B3 (Dr.KGH+Dr.AKG) 21EC384 B1 (PGN+MC)		21EC32 SBS	21MAT31 PRJ			

Sl.No.	Course Code	Course Title	Initials of the Course Instructor	Name of the Course Instructor
1	21MAT31	Mathematics Course	PRJ	Sri. Punith Gowda R J
	21MATDIP31	Additional Mathematics-I	Dr. SMC, OGD	Dr. Shanmuka M C. Kum. Deeca OG
2	21EC32	Digital System Design using Verilog IPCC	SBS	Smt. Sumana B S
3	21EC33	Basic Signal Processing IPCC	Dr. KGH	Dr. Kirankumar G H
4	21EC34	Analog Electronic Circuits	SWS	Smt. Swetha S
5	21ECL35	Analog & Digital Electronics Lab	MC	Sri. Mahendrachari
6	21UH36	Social Connect and Responsibility	SBS	Smt. Sumana B S
7	21KSK37	Sanskritika Kannada	SPH	Smt. Shanthala P H
	21KBK37	Balake Kannada	MR	Sri. Mruthunjaya
		Ability Enhancement Course - III		
8	21EC384	LabVIEW Programming Basics	PGN	Smt. Poornima G N

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Time Table Incharge

S. Thirug. S.  
30.10.2022  
Program Co-ordinator

  
Principal

Vision of the Department

To be in the forefront in providing quality technical education and research in Electronics & Communication Engineering to produce skilled professionals to cater to the challenges of the society