



Bapuji Educational Association®
Bapuji Institute of Engineering and Technology
(Affiliated to Visvesvaraya Technological University, Belagavi)
NAAC Accredited with 'A' Grade



tecnic components

<http://www.bietdvg.edu> P.O.Box.No.325, Shamanur Road, Davangere-577004, Karnataka

INTERNAL QUALITY ASSURANCE CELL (IQAC)



**National Level One Week Online Faculty Development Program on
Introduction to Microchip FPGA
19th to 23rd July 2021**



Organized by

Department of Electronics & Communication Engineering

Patrons

Dr. Shamnur Shivashankarappa
Honorary Secretary, BEA
& Chairman, BIET



Sri S S Mallikarjun
Joint Secretary, BEA



Sri. A. S. Niranjan
Treasurer, BEA



*Scan to
Register*



*Free
Registration*

Resource Persons



Mr. Harsha Manjunath,
EC Alumini 2002



Mr. Bhanu Krishna Reddy



Convenor
Dr. G. S. Sunitha, Professor & Head,
Dept. of E&CE, BIET



Advisory Committee

Prof. Y. Vrushabhendrappa
Director, BIET



Dr. H B Aravind
Principal, BIET



Dr. Jayadevappa B M
Professor, Dept. of E&CE, BIET

Coordinators :

Dr. Leela G H,
Dr. Nirmala S O,
Mrs. Savithri G R

Registration Details

No registration fee

Registration link:

<https://forms.gle/ky48CfKKB9Lu4CQh8>

Meeting link will be shared through E-mail/WhatsApp for registered participants.

E-Certificate will be provided for all the registered participants who attend all the sessions, participate in quiz and provide feedback of FDP.

Important Dates:

Last Date for Registration: 16th July 2021

FDP Dates: 19th to 23rd July 2021

Address for Correspondence:

email: bietece1979@gmail.com

Mobile: 9844474712, 9481360590, 9980456759

Resource Persons

Mr. Harsha Manjunath,
Senior Field Application Engineer,
Tecnomic Components Pvt. Ltd., Bengaluru
EC Alumni 2002

Mr. Bhanu Krishna Reddy,
Field Application Engineer,
Tecnomic Component Pvt. Ltd., Bengaluru

PATRONS

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Professor, Dept. of E&CE, BIET

Convener

Dr. G. S. Sunitha
Professor & Head, Dept. of E&CE, BIET

Coordinators

Dr. Leela G H, Associate Professor
Dr. Nimala S O, Associate Professor
Mrs. Savithri GR, Assistant Professor

Organizing Committee

Dr. Avinash K. G.	Mr. Ranjith Patil N
Mr. Prakash K. M.	Mr. Lingaraju
Mr. Kiran Kumar G. H.	Mrs. Poornima GN
Mrs. Sama K. G.	Mrs. Saman B S
Mrs. Radhika Priya Y. R	Mr. Yogesh K O
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Mrs. Bhagya S	Mrs. Vanishree H V
Mr. Ali I. K	Mr. Kantharaj S P
Mr. Ganes K.	Mr. Umaphathy S. P
Mrs. Nirmala G	Mr. V.S. Patil
Mrs. Shwetha S.	Mr. Girish M
Mr. Mahendrachari	

Venue:

Research Lab, 3rd Floor
E&CE Dept., Administrative Block.

Bapuji Educational Association @
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Recognized by UGC under 2(F) and 12(B) <http://www.bietdvj.edu>
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ISTE, Faculty Chapter BIET, IETE Shivamogga Center
&
Tecnomic Components Pvt. Ltd., Bengaluru

About the Institute

Bapuji Institute of Engineering and Technology was started in the year 1979 with the initiative and foresight of Bapuji Educational Association. The institute has been recognized by AICTE and accredited by NAAC with 'A' Grade, and also accredited by NBA and Institution of Engineers. The Institute has grown by leaps and bounds, and at present offers 10 UG and 4 PG programmes in Engineering, Technology and Management. The college has 13 recognized research centers. In addition to academic activities, the college undertakes consultancy works in Civil, Environmental, Mechanical, Electrical & Electronics and other fields of Engineering.

Vision of the Institute

To be a center of excellence recognized nationally and internationally, in distinctive areas of engineering education and research, based on a culture of innovation and invention.

Mission of the Institute

BIET contributes to the growth and development of its students by imparting a broad based engineering education and empowering them to be successful in their chosen field by inculcating in them positive approach, leadership qualities and ethical values.

About the Department

The department of Electronics & Communication Engineering was incepted in the year 1979 with a vision to promote and maintain academic & professional excellence in technical education and research field. The department was accredited three times by National Board of Accreditation (NBA),

AICTE, New Delhi. The department is recognized as research center in the year 2020.

Department has executed projects sponsored by AICTE, New Delhi and has carried out about 100 KSCST sponsored projects. The department has excellent infrastructure, well equipped laboratories and departmental library. The department is equipped with LabVIEW, MATLAB, DSP trainers and Evaluation Modules with Code Composer Studio, Microcontrollers Trainers with Flash Modules, OFC, MIC, Digital Communication modules. Open source softwares like NCTUNS, Dev C++, Multi Sim, Proteus, EDA Playground, Electric VLSI Design System, Keil, Scilab are also available. Department have well qualified faculty who are members of IEEE, IE, IETE and ISTE professional bodies.

Vision of the Department

To be in the forefront in providing quality technical education and research in Electronics & Communication Engineering to produce skilled professionals to cater to the challenges of the society.

Mission of the Department

M1. To facilitate the students with profound technical knowledge through effective teaching learning process for a successful career.

M2. To impart quality education to strengthen students to meet the industry standards and face confidently the challenges in the program.

M3. To develop the essence of innovation and research among students and faculty by providing infrastructure and a conducive environment.

M4. To inculcate the student community with ethical values, needs. Communication skills, leadership

qualities, entrepreneurial skills and lifelong learning to meet the societal

About tecnomic

M/s. Tecnomic is one of the trusted regional demand creation distributor in South East Asia and India for some of the leading brands in the global electronic component industry. Tecnomic Product line caters to diverse application segments such as Industrial, Power, Communications, RF and Microwave, Military, and Aerospace Electronics. The customers include all major ODM/OEMs IDH, and EMS companies in South East Asia and India.

About the FDP

The FDP is aimed to provide an overview on Microchip FPGA.

Workshop will focus on following topics:

- Introduction to Microchip FPGA and Libero SOC tool flow.
- Introduction to softconsole and Board & example demonstration.
- Introduction to AMBA Bus and sample demonstration on AMBA wrapper on board.
- Introduction to ARINC 429 Protocol and introduction to 1553 protocol
- Demonstration on FPGA features like Ethernet, spi, PCIe.

Participants

Faculty members from ECE / TCE / EEE / E&I / CSE / ISE departments of Engineering college can attend the workshop. PG students from E&CE department can also attend.