One Week **Faculty Development Program**
on
Embarking on Open Source Software in an Engineering & Research Journey

(Blended Mode)
19th – 24th April 2022

Registration Form (Offline Participants)

Name:

Department:

College address:

Email ID:

Mobile No:

Signature & Date:

Principal Signature

Scan here to Register

Online Registration form link:

https://forms.gle/A2BScxR6hQfx582Y9

Chief Patrons
Dr. Shamnur Shivashankarappa
Honorary Secretary, BEA & Chairman, BIET
Sri. S. S. Mallikarjun, Joint Secretary, BEA
Dr. Karisiddappa, Vice Chancellor, VTU

Patrons
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Dr. B. E. Rangaswamy, Registrar (Evaluation), VTU
Smt. M. A. Sapna, Finance Officer, VTU
Prof. Y. Vrushabhendrappa Director, BIET
Dr. H. B. Aravind, Principal, BIET

VTU FOSS Group Coordinators
Dr. Rashmi Rachh, Assoc. Professor, Dept. of CS&E
Dr. Poornima G Patil, Asst. Professor., Dept. of CS&E

Resource Persons
Mr. Nagaraj G K, TPM, Tata Communications Ltd., Bangalore.
Mr. Dinesh Annayya, Director in Engineering, Intel, Bangalore.
Mr. Prakash S, Director, Products and Technology, Bangalore.
Mr. Prasanna Kumar V G, Sr. Director, CAPGEMINI, Bangalore.
Mr. Harsha M, Sr. FAE, Tecnomic Components, Bangalore.
Mr. Sunil Sheelvanth, IC Design Engineer, Switzerland.
Mr. Hareesha K, Co-Founder & CEO, Sloki Software, Bangalore.
Mr. Sridhar M, Lead Firmware Engineer, AMD, Texas, USA.
Mr. Siddanagouda G N, Manager, Western Digital, Bangalore.
Mr. Dileep H, Technical Lead, Sloki Software, Bangalore.
Mr. Phani Kumar, Lead System Sw. Engineer, Analog Devices, Bangalore.
Mr. Ganesh K, Assistant Professor, Dept. of E&CE.
Mrs. Shwetha S, Assistant Professor, Dept. of E&CE.
Mr. Ranjith Patil N, Assistant Professor, Dept. of E&CE.
Mr. Lingaraj, Assistant Professor, Dept. of E&CE.

Co-coordinators
Dr. B. M. Jayadevappa, Professor
Dr. G H Leela, Associate Professor
Dr. Nirmala S O, Associate Professor
Mrs. Shwetha S, Assistant Professor
Mr. Ranjith Patil N, Assistant Professor
Mr. Kantharaj S P, Assistant Professor
Mrs. Sucharitha S D, Assistant Professor

Organizing Members
Faculty Members, Dept. of E&CE.

Bapuji Educational Association®
Bapuji Institute of Engineering and Technology, Davangere-577004
Accredited by NAAC with 'A' Grade

Department of Electronics & Communication Engineering, Research Center
(Accredited by NBA)

in association with
FOSS Group, Visvesvaraya Technological University, Belagavi

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Coordinator
Dr. G. S. Sunitha, Professor & Head

About BIET: Bapuji Institute of Engineering and Technology was started in the year 1979 with the initiative and foresight of Bapuji Educational Association. The institute has been recognized by AICTE and accredited by NAAC with 'A' Grade, and Institution of Engineers. The Institute has grown by leaps and bounds, and at present offers 09 UG and 04 PG programmes in Engineering, Technology and Management. The college has 14 recognized research centers. In addition to academic activities, the college undertakes consultancy works in Civil, Environmental, Mechanical, Electrical & Electronics and other fields of Engineering.

About the Department: The department of Electronics & Communication Engineering was established in the year 1979 with a vision to promote and maintain academic & professional excellence in technical education and research field. The department is accredited for 4th time by National Board of Accreditation (NBA), AICTE, New Delhi in the year 2022. Department has to its credit 36 ranks. The department has well qualified faculty members and dedicated supporting staff. The department has excellent infrastructure, well equipped laboratories and departmental library. The Laboratories are equipped with Cadence, Xilinx Software with CPLD/FPGA kits, MATLAB, DSP trainers and Evaluation Modules with Code Composer Studio, ARM Cortex M3 Boards, Microcontroller Trainers with Flash Modules, LabVIEW, OFC kits, MIC, Microwave Testbench, Analog and Digital Communication modules. Department is recognized as Research Centre by VTU Belagavi since 2020.

VTU FOSS Group: VTU has been promoting the use of Free and Open Source Software (FOSS) among faculty and students through VTU FOSS group. A FOSS advisory body has been constituted with a chairperson and members/invitees. This advisory body is responsible for planning and executing all FOSS initiatives of VTU. VTU FOSS Group is encouraging institutions to conduct FOSS programs and financial assistance is given for FOSS related programs.

About VTU: VTU is one of the largest Premier Technological Universities in India with 23 years of Tradition of Excellence in Engineering & Technical Education, Research and Innovations. It came into existence in the year 1998 to cater the needs of Indian Industries for trained technical manpower with practical experience and sound theoretical knowledge. University has successfully achieved the tremendous task of bringing various colleges affiliated earlier to different universities, different syllabi, different procedures and different traditions under one umbrella. The University having 201 affiliated colleges, 1 constituent college and 24 autonomous colleges with undergraduate courses in 35 disciplines, PG program in 94 disciplines and Ph. D & M.Sc. (Engg.) with research programs in 592 departments with over 4 lakhs engineering students studying in various affiliated colleges. The university has 13 quality improvement program (QIP) centers in various affiliated colleges and 16 extension centers for offering PG programs. The University is making steady progress in developing and providing best technical environment for education, implementation of NEP 2020 and will continue to serve the nation in the coming years. Equipped with the extremely rich resources of an outstanding student body and faculty strong partnerships with business, industry. Government & alumni and friends, VTU is designing a future of global pre-eminence, leadership and service. Recently, VTU has implemented number of innovative initiatives to promote planned and sustainable development of technical education in the country.

About FDP: This FDP is jointly organized with VTU-FOSS Group, Belgavi for the enhancement of knowledge in FOSS (Free and Open Source Software). The FDP focusses on free open source tools used in Embedded/ VLSI domain. Free software is considered to be an alternative to proprietary one, as it improves flexibility and saves cost. Experts from Company are invited for sharing the knowledge.

Objectives:
- Provides an opportunity for researcher/faculty to familiarize with Free and Open Source Software (FOSS) in Embedded and VLSI domain.
- Create an avenue to interact with Industry experts.
- Present the enormous potential and progress of the Embedded and VLSI domain.

Expected outcomes:
- Knowledge Enhancement in Free and Open Source Software in Embedded and VLSI domain.
- Utilize the knowledge gained in FOSS resources in Embedded and VLSI domain for research / project.

Who can participate?
- Faculty / Research Scholars of all AICTE affiliated institutions.

Registration Fee	₹200/-
Last Date for Registration	13th April 2022
Venue	Research Centre, Dept. of E&CE
Contact Details	Mr. Ranjith Patil N Mob: 9538820459 Mr. Kantharaj S P Mob: 9916019482

For offline participants:
- No TA DA will be provided
- Working lunch will be provided.
- Accommodation will be provided with nominal charges.

Account Details:

Registration Fee may be sent through PhonePe/Google Pay to the number **9916019482**.