

CURRICULUM VITAE

Vishwanath V.K.

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OBJECTIVE

To earn a permanent position in the field of computer science, which would hone my skills and support my education.

EXPERIENCE

Total teaching experience of 17 years

- Worked as a lecturer for a period of 2 Semesters/ 1 year in the Bapuji Institute of Engineering & Technology, Davangere.
- as a lecturer at S.J.M.Institute of Technology, Chitradurga from Sep-2004 to Feb-2007,
- as a senior lecturer at S.J.M.Institute of Technology, Chitradurga from March 2007 to Jan 2008
- as an asst.prof & H.O.D. at S.J.M.Institute of Technology, Chitradurga from Feb 2008 to Jan 2010
- as an asst.prof. at S.J.M.Institute of Technology, Chitradurga from feb 2010 to Aug. 2011
- as an asst.prof. at Jain Institute of Technology,davangere from aug.2011 to Mar 2016.
- as an asst.prof. at KLE Institute of Technology,Hubballi from aug.2016 to Apr 2018.
- as a guest lecturer at UBDTCE, Davangere from Sep.2018 to Aug.2021.
- as an Asst.Prof. at GM Institute of Technology,Davangere, from Oct.2021 to Nov.2022
- Presently working as an assistant professor at BIET,Davangere from Nov.2022 to till date

EDUCATIONAL QUALIFICATIONS

EXAMINATION PASSED	BRANCH/SPECIALIZATION	INSTITUTION	BOARD/UNIVERSITY	MONTH&YEAR OF PASSING	CLASS
S.S.L.C.		GOVT.FORT HIGH SCHOOL CHITRADUGA	S.S.L.C. BOARD	APR, 1995	FIRST CLASS (83.61%)
P.U.C.		GOVT.SCIENCE COLLEGE CHITRADURGA	P.U.BOARD	MAY, 1997	FIRST CLASS (81%)
B.E.	CS&E	KALPATARU INSTITUTE OF TECHNOLOGY TIPTUR	BANGALORE UNIVERSITY, BANGALORE	SEP,2001	FCD (74.6%)
MTech.	CS&E	NMAMIT NITTE	V.T.U., BELGAUM	JAN, 2007	FCD (71.5%)
Ph.D.	CS&E/Edge Computing	-----	V.T.U., BELGAUM	Pursuing	

PROGRAMMING SKILLS

LANGUAGES : C, C++, SQL, ASP.NET, VB.NET
OPERATING SYSTEMS : WINDOWS, LINUX

SUBJECTS TAUGHT IN ACADEMICS:

Data structures with C, Analysis and Design of Algorithms, RDBMS, Data communication, Computer Networks, Computer Organization, Network Management Systems, Computer Concepts & C Programming, Internet of Things, Cloud Computing, Web Technology and its applications, Microcontroller and Embedded systems

Final Year B.E. Project :

8th Semester

- Title** : SYNTAX ANALYZER FOR C LANGUAGE
Software : Turbo C
Platform : Windows 9X
Description : This project is basically a compiler design project developed in C. Here only the 2 phases of the compiler i.e. the Lexical Analysis & Syntax Analysis phases are implemented, i.e. the Scanner & Parser are developed in our project. This is actually a Syntax analyzer for the C statements like for(), if(), while(), main(), printf(), scanf() etc.

Final Year M.Tech Project:

- Title** : ACTIVITY MANAGEMENT
Software : ASP .NET, VB .NET, MS SQL Server 2000, IIS
Platform : .NET
Description : An automation software for managing the various activities of a Life Insurance Corporation company

PAPERS PRESENTED IN NATIONAL/INTERNATIONAL CONFERENCE

SL. NO	TITLE OF THE PAPER	NATIONAL/INTERNATIONAL	TITLE OF THE CONFERENCE	DATE	ORGANIZER
1	Emphasizing Distinct Use Case Examples of Edge Computing	International	DST, SERB sponsored AICDMB	Feb 16-17 2022	Dept. of CS&E, VVCE, Mysuru.

FACULTY DEVELOPMENT PROGRAMMES/WORKSHOP CONDUCTED

SL. NO	NAME OF THE WORKSHOP	YEAR/DURATION	ORGANIZERS
1	DBMS Boot camp	Jan.2022/2 days	Dept. of IS&E, GMIT, Davangere
2	Think Python	jun.2022/3 days	Dept. of IS&E, GMIT, Davangere

FACULTY DEVELOPMENT PROGRAMMES/WORKSHOP ATTENDED

SL NO	NAME OF TRAINING/WORKSHOP	YEAR /DURATION	ORGANIZERS
1	Refresher Program on Design Thinking	2022/ 7Days	VVCE, Mysore
2	FDP on Blockchain Technologies	2020/ 5days	Reva University, Bengaluru
3	STTP on Frontiers of Computer Science.	2020/ 5days	Jain Institute of Technology, Davangere
4	Webinar on Agile Technology	2020/ 1day	East West College of Engineering, Bengaluru.
5	National Cyber Defense Summit	2017/ 2days	BITS Pilani- Goa Campus

Assignments Undertaken

- Worked as Examination Management Systems(EMS) and Question Paper Delivery System coordinator at Jain Institute of Technology, Davangere
- Worked on 4th Criteria of NBA, at GM Institute of Technology, Davangere.
- Written VGST Proposal like K-FIST and SERB Proposal for conducting High End Workshop.

PERSONAL DETAILS

NAME : VISHWANATH V.K.
DATE OF BIRTH : 6TH MARCH 1980.
SEX : MALE
MARITAL STATUS : MARRIED
FATHERS NAME : KOTRAPPA V.J.
CARE OF ADDRESS : #561/55,
SARASWATI NAGARA 'B' BLOCK
DAVANGERE -577504
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LANGUAGES KNOWN : KANNADA, ENGLISH, HINDI.

(VISHWANATH V.K.)