

Smt. Nirmala G.

Asst. Professor

Department of E & C E

BIET, Davangere [nirmalag2801@yahoo.co.in]



Work Experience: 14Years

- **Worked as a lecturer in E&CE Department of BIET, Davangere from Nov 1987 to Jan 1990.**
- **Worked as faculty in Aptech, New Delhi May 1994 to Dec 1994.**
- **Worked as Director in Seventh Avenue Exports, Bangalore from May 1995 to July 1997.**
- **Working as Asst. Professor in E&CE Department of Bapuji Institute of Engineering and Technology, Davangere since Aug 2010.**

Educational Qualification:

- **M.Tech. [Computer Science & Engineering] from Bapuji Institute of Engineering and Technology, Davangere, VTU in the year 2014.**
- **B.E. [Electronics & Communication] from Bapuji Institute of Engineering and Technology, Davangere, Mysore University in the year 1987.**
- **B.Sc. from Desheeya Vidya Shaala Senior College, Shimoga, Mysore University in the year 1983.**

Awards:

As a Student

- **Received Padmabhushan Dr. M.C. Modi Gold Medal for “Top Student of Electronics & Communication” of BIET in the year 1986-87.**
- **Received Prof. G.S. Sivanna Gold Medal for “Best Outgoing Student of the Institute “ of BIET in the year 1986-87.**

As a Teacher

- **Obtained cent percent result in Computer Communication Networks (15EC71) in 2018.**
- **Obtained cent percent result in Digital Switching Systems (10EC82) in 2017.**

- Obtained cent percent result in Management & Entrepreneurship (06EC61 & 10EC61) of VI Semester twice in 2011 & 2012.
- Obtained cent percent result in Programming in C++(06EC661) of VI semester 2012.

Paper Published:

- Has published a paper titled “Faculty Performance Evaluation Using Data Mining” in the International Journal for Advanced Research in Computer Science & Technology Vol. 2 Issue 3 Ver. 1 July-Sept. 2014.

Membership:

- Associate Member of Institute of Engineers (AMIE)-66340 (Life Member).
- Life Member of Indian Society for Technical Education-LM 89378

Core Subjects Taught:

Basic Electronics, Digital Switching Systems, Programming in C++, Linear Integrated Circuits & Applications, Operating Systems, Computer Communication Networks, Analog Communication, Microcontroller, Microprocessor, Verilog HDL, Computer Organization and Architecture, Engineering Statistics and Linear Algebra.

Seminars/Workshops/Events Conducted:

- *Conducted a Value Added Training Programme on “Object Oriented Programming using C++”, from 6th November to 11th November 2019 for 3rd, 5th and 7th semester students.*
- *Conducted a Value Added Training Programme on “Object Oriented Programming using C++”, from 7th September to 18th November 2017 for 5th semester students.*
- *Conducted a five day Value Added Training Programme on “Arduino & LabVIEW”, for second year students at BIET, from 25th July to 29th July 2016.*
- *Conducted a five day Value Added Training Programme on “Object Oriented Programming using C++”, from 6th November to 10th November 2017 for 3rd, 5th and 7th semester students.*

Seminar/Workshops Attended:

- *Attended “Covid-19 General Awareness Quiz” organized by IS & E Department, BIET, Davangere on 3rd May 2020.*
- *Attended One Week AICTE-VTU Joint Training Programme for Teachers on “An Overview of Teaching Techniques in Engineering Statistics and Linear Algebra” between 6th to 10th January 2020 at VTU Centre for Post Graduate Studies, Bengaluru Region, Muddenahalli, Chikkaballapur.*
- *Attended One Day Workshop on eSim, a First Course in the IoT Series for Teachers on Saturday, 21 September 2019 organized by the Teaching Learning Centre (ICT) at IIT Bombay, a scheme of 'Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching' (PMMMNTT), an initiative of the Ministry of Human Resource Development, Government of India, at Information Science & Engineering. Department, BIET.*
- *Attended two day Faculty Development Program on “Arm Cortex M3 Microcontroller” on 23rd and 24th of January 2018 at BIET in association with ISTE, BIET Faculty Chapter and Advanced Electronic Systems, Bangalore.*
- *Attended two day Faculty Development Program on “System Design using Vivado Design Suite and Zynq –7000 SoC” in association with ISTE-BIET Faculty Chapter, Davangere and CoreEL Technologies, Bengaluru on 15th and 16th June 2017.*
- *Attended two day Faculty Development Program on "Image Processing and Its Applications" in association with ISTE student chapter, BIET, ISTE Shimogga Sub-section, Shimogga and IETE, Karnataka on 29th and 30th Oct 2014.*
- *Attended “Feel Teacher” Learning and Development Intervention organized by CLHRD, Mangalore conducted from 19th April to 21st April 2013 at BIET, Davangere.*
- *Attended Deep Dive Faculty Enablement Program on Foundation Program 3.1 conducted by Infosys Campus Connect from 28th Jan to 1st Feb 2013 at BIET.*

- *Attended Two-day FDP on “Labview and its Applications” by Texas Instruments, Bangalore, at E&CE Department, BIET, Davangere on August 2nd & 3rd 2012.*
- *Attended Two-day FDP on “Low Power Embedded Systems using Texas Instruments MSP 430 Microcontrollers” by Cranes Software International Ltd. And Texas Instruments India University Program, B’lore on 9th and 10th Feb 2012.*
- *Attended Two-day FDP on “Image Processing & its Applications” organized under ISTE-BIET chapter in association with IETE subcenter Shivamogga at BIET on 29th & 30th October 2014.*
- *Participated in two days workshop on “Women Empowerment” held on 18th & 19th December 2014, organized by IS & E Department, BIET.*
- *Participated in the “Workshop on Research Methodology” organized by Research & Development cell in association with ISTE-BIET chapter, Bapuji Institute of Engineering & Technology, Davangere held on 28th October 2015.*
- *Attended one day seminar on “ROLE OF WOMEN IN SOCIETAL REFORMS” organized by Department of Information Science and Engineering in association with ISTE Faculty Chapter, ISTE Student Chapter, NSS, YRCW, BIET, Davangere on 10th March 2017.*
- *Attended two day workshop on “System Design using Vivado Design Suite and Zynq –7000 SoC” organized by Department of Electronics & Communication Engineering, ISTE-BIET Faculty Chapter, Davangere and CoreEL Technologies, Bengaluru on 15th and 16th June 2017.*

Conference:

- *Attended a National Conference on “Recent Advances in Electronics & Communication Engg.” organized by E & CE department on 13th October 2017 for faculty, research scholar and PG students at BIET, Davangere.*

Short Term Course:

- *Participated and successfully completed five days short term course on*

”Microcontroller Applications” under Quality Improvement Programme of All India Council for Technical Education, New Delhi from 11th July to 15th July 2016 at Indian Institute of Science, Bangalore.