

Dr.Sunitha G S

**Professor & Head
Department of E & C E
BIET, Davangere
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Work Experience :28 years

- **Joined as a lecturer in E&CE Department of Bapuji Institute of Engineering and Technology Davangere from Dec 1990.**
- **Deputed for higher studies from the college to Karnataka Regional Engineering College, Surathkal in 1994.**
- **After the completion of Masters Degree, i.e., from 1996, continued services in E&CE Department of Bapuji Institute of Engineering & Technology, Davangere.**
- **Promoted as Senior Lecturer from Mar 2005.**
- **Promoted as Assistant Professor from Aug 2007 in the department of Electronics & Communication Engineering.**
- **Promoted as Professor from Nov 2011 in the department of E & C E.**
- **Working as Professor & Head from Feb 2013 in the department of E & C E.**

Educational Qualification:

- **Ph.D [Electronics & Communication Engineering] from Kuvempu University, 2011.**
- **M.Tech [Digital Electronics & Advanced Communication] from KREC, Surathkal , Mangalore University, 1996, First Class with Distinction.**

- **B.E [Electronics & Communication Engineering] from Govt. BDT College of Engineering & Technology, Davangere, Mysore University , 1990 with First Class.**

Dissertation Details:

“A Study of ATM Networks and Their Performance Analysis”

Roles and Responsibilities:

- **Counselor for IGNOU, BIET Center, Davangere**
- **Board of Examiner for UG studies of Kuvempu University for the Year 2005-2006.**
- **Served in the management committee as Faculty member for ISTE-SC for the year 2006-07.**
- **Board of Examiner for UG and PG studies of Kuvempu University for the Year 2006-2007.**
- **Resource person for handling BCA classes at Bapuji Hi-Tech Education, Davangere College.**
- **Facilitator for Infosys initiated Campus Connect “Soft Skills” Training Program.**
- **Chairperson, Board of Studies of BSc Electronics in Davangere University for the period 2012 - 2014.**
- **Appointed as Member for BOS of Department of E&CE, Malnad College of Engineering, Hassan for the academic year 2013 - 2014.**
- **Involved in deployment of EPABX/Intercom & CCTV of the institute in the year 2013-2014.**

- Nominated as Executive Committee member for Institution of Electronics & Telecommunication Engineers, Shimoga sub center for the academic year 2014-15.
- Worked as co-coordinator for four days workshop on “Training of Evaluators on Outcome Based Education and Accreditation” jointly organized by NBA, New Delhi and Bapuji Institute of Engineering and Technology , Davangere on 5th Mar (Phase I) and 21st to 23rd Mar 2014 (Phase II).
- Appointed as Member of Board of Examiners of EC/TE (Composite) Board of VTU, Belagavi for the academic period 2017-2018.
- Chaired the session for paper presentation in International Conference on “Multimedia Processing, Communications and Information Technology”- MPCIT-2018 organized by Jawaharlal Nehru National College of Engineering in association with Institute for Exploring Advances in Engineering(IEAE) on 9th June 2018, Shimoga.

Awards:

- Seven student projects sponsored by KSCST, Bangalore
- Received Best Teacher Award and silver medal (Eight times) from Bapuji Institute of Engineering and Technology, Davangere for having obtained cent percent results in the subjects taught for the academic year 2001-2002, 2002-2003, 2004-2005, 2006-2007, 2007-2008 , 20102011 and 2011-2012(Twice).

Membership:

- Life Member of Indian Society for Technical Education : LM23571
- Life Member of Institute of Electronics & Telecommunication Engineers : F-219021

Core Subjects Taught:

Digital Switching Systems, Microcontrollers, Digital Signal Processing, DSP Algorithms & Architecture, Computer Communication Networks, Multimedia Communication, Information Theory & Coding, VLSI, Linear Integrated Circuits & its Applications, Line Communication Systems, Computer Organization, Analog Electronics, Digital Electronics, Pulse Techniques.

Expert Lectures Delivered:

- **Delivered a Lecture on “Synchronization, Multiplexing and Multiple Access” on 18th Jul 2006 and “Data Communication & Networks” on 21st Jul 2006 in Short Term Training Program on “Digital communication” held from 17th Jul to 21st Jul 2006 sponsored by ISTE and organized by Department of Electronics and Communication, Bapuji polytechnic, Davangere.**
- **Delivered a Lecture on “Digital Logic” on 4th July 2006 in “Short Term Training Program on Mechatronics” held from 3rd July to 7th July 2006 sponsored by ISTE and organized by Department of Mechanical Engineering, Bapuji polytechnic, Davangere.**
- **Delivered a Lecture on “HDL” on 18th July 2008 organized by Department of Electronics and Communication, Bapuji polytechnic, Davangere.**
- **Delivered a Lecture on “Behavioural Description” under a Three days Workshop on Digital System Design using HDL-Synthesis & Simulation using Test Benches, on 16th to 18th Apr 2009, at BIET.**
- **Delivered a Lecture on “Communication System” in the Orientation Programme in Physics on 25th August 2015 for faculty of Physics under Davangere District PU Physics lecturer’s Forum at Sri Siddaganga Science PU College, Davangere.**
- **Delivered a Lecture “Telecommunication Technologies” for 9th standard studying Rural High School students on the occasion of Technology Barrier**

Reduction Program (TBRP) under the foundation for Advancement of Education & Research (FAER) on 29th April 2016 at BIET, Davangere.

- **Delivered a Lecture “Electronics Around Us” for 9th standard studying Rural High School students on the occasion of Technology Barrier Reduction Program (TBRP) under the foundation for Advancement of Education & Research (FAER) on 2nd May 2018 at BIET, Davangere.**

Seminars/Workshops/Events Organized:

- **Worked as Coordinator for the Certification course on “VHDL Coding for Synthesis”, organized by SIMS, Bangalore and E&C Dept B.I.E.T, Davangere from 4th to 19th Feb 2000.**
- **Worked as Coordinator for the Certification course on “Embedded Systems & Design” organized by B.I.E.T and C.I.T.L. Bangalore, under Embedded Techpark from 8th Apr to 23rd May 2004 (Two weeks).**
- **Organized a Workshop on “Digital System Design using HDL – Synthesis & Simulation using Test Benches” from 16th to 18th April 2009, at BIET, Davangere.**
- **Co-ordinator for “ Low power embedded system using Texas Instrument MSP 430” organized by E&CE, BIET in association with TI University program and Cranes Software Intl. Ltd on 9th and 10th Feb 2012.**
- **Organised a Two days work shop on “LABView and its Applications” organized by BIET Davangere and Trident Technologies Bangalore on 2nd and 3rd August 2012.**
- **Organized a seminar on “Image Processing and its applications” for faculty and students on 27th April 2012.**
- **Organized Two day workshop on “PCB Etching and Multi-utility Microcontroller Board Design” for students on 23rd and 24th March 2013.**

- **Organized seminar on “Recent Trends in Optical Fiber Communication and Networks” for pre-final year and final year students by Dr. P C Srikanth, HOD, Malnad College of Engineering, Hassan on 5th Sep 2013.**
- **Organized Two day workshop on “PCB design and Application of Microcontroller” for students from 20th to 22nd September 2013.**
- **Organized two days training program on “Robotics and AVR Microcontroller” for pre-final year students of BIET, JIT and UBDTCE, Davangere in association with Pravega, IISC-Bangalore and UV Soft Technologies Pvt. Ltd., Delhi, on 28th and 29th Sep 2013.**
- **Monitored two day technical event “Funtronics” organized by Department of E&CE in association with ISTE student’s chapter on 10th and 11th Oct 2013.**
- **Organized a two day workshop on “Cadence Tool” for faculties of E&CE, E&I and BME, resourced by Mr. Vijay Kumar P S, Cadence Design Systems India Pvt. Ltd., Bangalore on 24th and 25th Oct 2013.**
Organized a technical talk on “Digital EPABX/Intercom System” resourced by Mr. Prasad Matt, Creative Communications, Davangere and Mr. Siddaraju from Compac TeleSystems, Bangalore on 28th Jan 2014.
- **Organized a seminar on “Introduction to Workshop on Embedded Systems” by MaMinno Technologies for pre-final year and final year students of Dept. of E&CE at Aryabhata Seminar hall on 28th Mar 2014.**
- **Organized a two day workshop on "Overview of VLSI and Cadence's Skill Automation" for final year students on 26th and 27th April 2014.**
- **Organized a technical talk on “Recent Trends in Wireless Communication” for students and faculties of Dept. of E&CE addressed by Mr. Richard Paul, Senior Technical Specialist, Alcatel Lucent, Bangalore on 19th Aug 2014.**
- **Organized a three day workshop "Trace the Copper-An Art of PCB Making" for pre-final year students. The same was conducted by a team of 13 final year students of Dept. of E&CE on 3rd, 4th and 5th Sep 2014.**

- Organized a two day workshop "ROBODUINO" for pre-final year students. The same was conducted by a team of 10 final year students of Dept. of E&CE on 22nd and 23rd Sep 2014.
- Co-ordinated a two day Faculty Development Program on "ImageProcessing and Its Applications" in association with ISTE Students' Chapter, BIET, ISTE, Shimoga Subsection, Shimoga and IETE,Karnataka Section on 29th and 30th Oct 2014.
- Arranged an interacting session for E&CE faculties with Dr. Uday Kumar, Raman Research Institute, Bangalore from on 31st Oct 2014.
- Arranged a Microsoft Technologies India Ltd. hosted webinar session for female students of E&CE, CSE and ISE all semester on "WOMEN IN TECH". The same was conducted by Mr. Lohith G K and team on 31st Oct 2014.
- Arranged a five day Value Added Training Programme on "Arduino & LabVIEW", for second year students at BIET, from 25th July to 29th July 2016.

Seminars/Workshops Attended :

Workshop:

- A National Level workshop on "VLSI & CAD for VLSI" organized by Industrial Consultancy & Research Center of BIT Bangalore, from 4th to 9th Sept. 2000.
- A "Certification course on VLSI & VHDL", organized by SIMS, Bangalore and E&C Dept B.I.E.T, Davangere from 10th Feb to 21st Mar 2001.
- A workshop on "Processor Architecture & Embedded Control System Design" organized by SDMCE, Dharwad, held on 18th May 2002.
- A course on "Embedded Systems & Design" organized by B.I.E.T and C.I.T.L. Bangalore, under Embedded Techpark from 8th Apr 2004 to 23rd May 2004 (two weeks).

- A workshop on “Quality of Service on Internet” held from 4th July to 9th July 2005 organised by Bapuji Institute of Engineering & Technology in collaboration with Council of Scientific and Industrial Research, New Delhi & VTU, Belagum.
- A VTU sponsored workshop on “Three day workshop on DSP” held from 7th Aug to 9th Aug 2005 at Bangalore Institute of Technology, Bangalore.
- A Five-day workshop on “Network Management & Network Simulation using NS2” held from 16th Aug to 20th Aug 2005 at NMAM Institute of Technology, Nitte.
- A “Training program on Learning Management Systems”, cosponsored by National program on Technology Enhanced Learning (NPTEL) and organized by Indian Institute of Science, Bangalore, from 2nd to 4th Mar. 2006.
- A workshop on “Tanner Tools. Pro- A Complete VLSI Design”, organized by E&C Dept., ISTE-chapter, BIET, Davangere and Sovereign Technologies, Bangalore on 16th Nov.2006.
- A workshop on “Embedded System Design”, held on 27th Apr 2007 organized by Industry Institute Partnership Cell and E&C forum , BIET, Davangere in association with Advanced Electronic Systems , Bangalore.
- A work shop on “Low power embedded system-MSP 430” organized by TI University program in association with Cranes Software International Ltd on 18th and 19th Jan 2012.
- A work shop on “Low power embedded system using Texas Instrument MSP 430” organized by E&CE, BIET in association with TI University program and Cranes Software International Ltd on 9th and 10th Feb 2012.

- A Two days work shop on “LABView and its Applications” organized by BIET Davangere and Trident Technologies Bangalore on 2nd and 3rd August 2012.
- A Two days work shop on “Xilinx DSP Design Flow Using System Generator” organized by RV College of Engineering, Bangalore and CoreEL Technologies Bangalore on 23rd and 24th August 2012.
- As BOE Representative attended one day workshop on AICTE Model Curriculum for First Year BE in Sahayadhri College of Engineering and Management, Mangalore on 19th May 2018.

Short Term Training Programme:

- A MHRD sponsored Short Term course under the joint Telematics program on “Applications of DSP in wireless Communications and Image Processing” organized by Electrical Engineering department from 11th Dec to 21th Dec 2001 at Indian Institute of Technology, Madras.
- A AICTE-ISTE Two week Short Term Training Programme on “Recent Advances in Biomedical Engineering” from 29th July to 08th Aug 2002 organized by department of Mechanical Engineering and Instrumentation Technology at Bapuji Institute of Engineering & Technology, Davangere.
- A Short Term Training Programme on “Trends in Broad Band Communication” from 30th Aug to 4th Sept 2004, sponsored by ISTE at R.V.College of Engineering, Bangalore.
- A Short Term Training Programme on “Train the Trainer program on soft skills” from 19th Mar to 22nd Mar 2012, Conducted by Infosys Limited, Mysore.

- Participated and successfully completed four days workshop on “ Training of Evaluators on Outcome Based Education and Accreditation” jointly organized by NBA, New Delhi and Bapuji Institute of Engineering and Technology , Davangere phase I webinar on 5th March and phase II from 21st to 23rd March 2014.
- Participated and successfully completed five days short term course on ”Microcontroller Applications” under Quality Improvement Programme of All India Council for Technnical Education, New Delhi from 11th July to 15th July 2016 at Indian Institute of Science, Bangalore.

Seminar:

- A National seminar on “Educational Scenario of the 21st Century” held on 03rd Feb 2007, organized by A.V. Kamalamma College for Women, Davangere.
- A Seminar on “Optical Networks” held on 10th Mar 2007,sponsored by TEQIP and organized by the department of studies in Electronics and communication Engineering at University B D T College of Engineering, Davangere.
- Attended One day seminar on “Markov Chains and its Applications” organized by the department of Master of Computer Applications, Bapuji Institute of Engineering and Technology held on 22nd April 2016.

Publications:

- Rakesh H.M, Dr. G.S. Sunitha, “Performance Comparison of Conventional multiplier with Vedic Multiplier using ISE simulator”, International Journal of Engineering and Manufacturing Science, ISSN 2249-3115 Volume 8, Number 1 (2018) pp. 95-103.
- Rakesh H.M, Dr. G.S. Sunitha, “Comparative analysis of 16 x 16 bit Vedic and Booth multipliers”, World Journal of Technology,

Research and Engineering(WJTER), Volume 3, Issue 1,pp.305-313, Jan 2018.

- **Rakesh H.M, Dr. G.S. Sunitha, “Design and Implementation of Vedic and Booth multiplier on spartan 3”, International Journal of VLSI Design, Microelectronics and Embedded System, Volume 2, Issue 2 ,pp.34-40, Dec 2017.**
- **Rakesh H.M, Dr. G.S. Sunitha “Design and Implementation of adder architectures and analysis of performance metrics”, International Journal of Electronics and Communication Engineering and Technology (IJECEET), Volume 8, Issue 5, pp.1–6, Sep-Oct 2017.**
- **Rakesh H.M, Dr. G.S. Sunitha, “Performance comparison of conventional multiplier with Vedic multiplier using ISE simulator”, National Conference on Recent Advances in Electronics and Communication Engineering NCRAECE, Davangere, Oct 2017**
- **Sunitha G S, Suresh Chandra Mohan, “A study of Quality of Service in high speed ATM Networks”, Proceedings of the National Conference on Computer Networks(NCCN 2009), Reva Institute of Technology & Management, Bangalore, pp. 105-107, Feb 2009.**
- **Sunitha G S, Suresh Chandra Mohan, “Performance Comparison of CODEC for ATM Networks using OPNET”, Proceedings National Conference on Electronics, Communications and Computers(NCECC 2009), IETE Navi Mumbai subcentre, pp. 59-61, Feb 2009.**
- **Sunitha G S, Suresh Chandra Mohan, “Performance Comparison of ATM and IP Network for Multiple Profiles using OPNET Simulator”, International Journal on Communications Antenna and Propagation(IRECAP), ISSN 2039-5086 Vol 1, No.5, Oct 2011,pp 471-477, Italy.**
- **Sunitha G. S, Suresh Chandra Mohan, “Performance Metrics of ATM & IP Network for Multiple Profiles with 99%Background Utilization of Link” International Journal of Electronics Communication and Computer Engineering(IJECCE), ISSN (Online): 2249–071X, ISSN (Print): 2278–4209 Vol 4, Issue 2, March-April 2013, pp 654-662.**

- **A course material titled “Error Control Coding” has been submitted to Center for National Academic Network for Continuing Education, AICTE, New Delhi, during the academic year 1996-1997.**
- **A course material titled “Digital Telephony” has been submitted to Center for National Academic Network for Continuing Education, AICTE , New Delhi, during the academic year 2000-2001.**