

# ***Dr.Sunitha G S***

**Professor & Head  
Department of E & C E  
BIET, Davangere  
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## ***Work Experience :31 years***

- Working as Professor & Head since Feb 2013
- Promoted as Professor from Nov 2011
- Promoted as Assistant Professor from Aug 2007
- Promoted as Senior Lecturer from Mar 2005
- After the completion of Masters Degree, i.e., from 1996, continued services in E&CE Department.
- Deputed for higher studies in 1994 to Karnataka Regional Engineering College, Surathkal.
- Joined as a lecturer on 1<sup>st</sup> Oct 1990 in Department of E&CE, Bapuji Institute of Engineering and Technology, Davangere.

## ***Educational Qualification:***

- Ph.D [Electronics & Communication Engineering] from Kuvempu University, 2011.
- M.Tech [Digital Electronics & Advanced Communication] from KREC, Surathkal , Mangalore University, 1996, First Class with Distinction.
- B.E [Electronics & Communication Engineering] from Govt. BDT College of Engineering & Technology, Davangere, Mysore University , 1990 with First Class.

## ***Dissertation Details:***

“A Study of ATM Networks and Their Performance Analysis”

## **Roles and Responsibilities:**

- Dr. G S Sunitha attended **VTU Board of Examinations (BOE)** meeting on 21<sup>st</sup> June 2021.
- Dr. G S Sunitha has attended Meeting of Board of Examiners to scrutinize Question papers for January 2021 VTU Examinations, VTU, Belagavi.
- Member of Local Inquiry Committee (LIC), VTU, Belagavi for the academic year 2019-2020 (Feb-March 2019).
- Member of Local Inquiry Committee (LIC), VTU, Belagavi for the academic year 2018-2019 (Feb-March 2018).
- Chaired the session for paper presentation in International Conference on “Multimedia Processing, Communications and Information Technology”- MPCIT- 2018 organized by Jawaharlal Nehru National College of Engineering in association with Institute for Exploring Advances in Engineering (IEAE) on 9<sup>th</sup> June 2018, Shimoga.
- Member of Board of Examiners of EC/TE (Composite) Board of VTU, Belagavi for the academic period 2017-2018.
- Chaired the session Delta NCETRM-2016, National Conference on Emerging Trends in Engineering, Research and Management, G M Institute of Engineering & Technology in 2016, Davangere.
- Chaired the sessions “Applications of Communication” & “Applied and Theoretical Computing” in International Conference on Applied and Theoretical Computing and Communication Technology(iCATccT-2015) at Bapuji Institute of Engineering & Technology on 29<sup>th</sup> & 31<sup>st</sup> October 2015, Davangere.
- Chairperson, Board of Studies of BSc Electronics in Davangere University for the period 2012 - 2014.
- Executive Committee member for Institution of Electronics & Telecommunication Engineers, Shimoga sub center for the academic year 2014-15 & 2015-16.
- Worked as co-coordinator for four days workshop on “Training of Evaluators on Outcome Based Education and Accreditation” jointly organized by NBA, New Delhi and Bapuji Institute of Engineering and Technology, Davangere on 5<sup>th</sup> Mar (Phase I) and 21<sup>st</sup> to 23<sup>rd</sup> Mar 2014 (Phase II).
- Member for BOS of Department of E&CE, Malnad College of Engineering, Hassan for the academic year 2013 - 2014.

- Involved in deployment of EPABX/Intercom & CCTV of the institute in the year 2013-2014.
- Chaired the session WIZTech- G M Institute of Engineering & Technology in 2013, Davangere.
- Served in the management committee as Faculty member for ISTE-SC for the year 2006-07.
- Member of Board of Examiner for UG and PG studies of Kuvempu University for the Year 2006-2007.
- Member of Board of Examiner for UG studies of Kuvempu University for the Year 2005-2006.
- Resource person for handling BCA classes at Bapuji Hi-Tech Education, Davangere College.
- Facilitator for Infosys initiated Campus Connect “Soft Skills” Training Program.
- Counselor for IGNOU, BIET Center, Davangere

### **Awards:**

- Under the guidance of Dr. G S Sunitha, Prof and HOD, Final year students presented a paper on **Design and verification of CMOS LDO regulator** at 2nd International Virtual Conference on Emerging Trends in Science, Engineering and Management-2021 on 16th and 17th July 2021 and got the Best Paper award.
- Seven KSCST Sponsored & one VTU sponsored Student projects.
- Received Best Teacher Award and silver medal (Eight times) from Bapuji Institute of Engineering and Technology, Davangere for having obtained cent percent results in the subjects taught for the academic year 2001-2002, 2002-2003, 2004-2005, 2006-2007, 2007-2008 , 2010-2011 and 2011-2012 (Twice).

### **Membership:**

- |                           |          |             |
|---------------------------|----------|-------------|
| • Life Member of ISTE:    | LM 23571 | Year - 1997 |
| • Fellow Member of IETE : | F-219021 | Year - 2010 |
| • Fellow of IE:           | 125432-0 | Year - 2019 |

## Core Subjects Taught:

Digital Communication , Digital Switching Systems, 8051 Microcontroller, Digital Signal Processing, DSP Algorithms & Architecture, Computer Communication Networks, Multimedia Communication, Information Theory & Coding, VLSI, Linear Integrated Circuits & its Applications, Line Communication Systems, Computer Organization, Analog Electronics, Digital Electronics, Pulse Techniques.

## Expert Lectures Delivered:

- Delivered a talk about **Scope of Electronics & Communication and Job Opportunities in Engineering** in the Student Induction Program (SIP)-2021, Phase II on 09<sup>th</sup> June 2021.
- Dr. G S Sunitha was invited as Resource Person for **Training High School Teachers under ATAL Tinkering Laboratories**, organized by District Institute for Education and Training on 22<sup>nd</sup> January 2021
- Emphasized about the E&CE department on 2<sup>nd</sup> Aug 2019 during First Year “**Student Induction Program**” (SIP) held from 1<sup>th</sup> to 10<sup>th</sup> August 2019.(SIP 1)
- Delivered a Lecture “**HighLights of E&CE Department**” on xx Aug 2019 & 14<sup>th</sup> Aug 2019 from 9 am to 9.30 am in SIP-19 & SIP-18 (Student Induction Program ) organized for first Year Engineering Students.
- Delivered a Lecture “**Electronics Around us**” for 9<sup>th</sup> standard studying Rural High School students on the occasion of Technology Barrier Reduction Program (TBRP) under the foundation for Advancement of Education & Research (FAER) on , 05<sup>th</sup> May 2018 & 6<sup>th</sup> May 2019 at BIET, Davangere.
- Delivered a Lecture “**Telecommunication Technologies**” for 9<sup>th</sup> standard studying Rural High School students on the occasion of Technology Barrier Reduction Program (TBRP) under the foundation for Advancement of Education & Research (FAER) on 29<sup>th</sup> April 2016, 05<sup>th</sup> May 2017 at BIET, Davangere.
- Delivered a Lecture in the Orientation Programme in Physics on Semiconductor Physics “**Communication System**” on 25<sup>th</sup> Aug 2015 for faculty of Physics at Sri Siddaganga Science PU College, Davangere.
- Delivered a lecture on Arduino in five day Value Added Training Programme on “**Arduino & LabVIEW**”, for second year students at BIET, from 25<sup>th</sup> to 29<sup>th</sup> Jul 2016.
- Delivered a Lecture on “**Behavioural Description**” under a Three days Workshop on Digital System Design using HDL-Synthesis & Simulation using Test Benches, on 16<sup>th</sup> to 18<sup>th</sup> Apr 2009, at BIET.
- Delivered a Lecture on “**HDL**” on 18<sup>th</sup> Jul 2008 organized by Department of Electronics and Communication, Bapuji polytechnic, Davangere.
- Delivered a Lecture on “Digital Logic” on 4<sup>th</sup> July 2006 in “Short Term Training Program on Mechatronics” held from 3<sup>rd</sup> July to 7<sup>th</sup> July 2006 sponsored by ISTE and organized by Department of Mechanical Engineering, Bapuji polytechnic, Davangere.
- Delivered a Lecture on “**Synchronization, Multiplexing and Multiple Access**” on 18<sup>th</sup> Jul 2006 and “**Data Communication & Networks**” on 21<sup>st</sup> Jul 2006 in Short Term Training Program on “Digital communication” held from 17<sup>th</sup> Jul to 21<sup>th</sup> Jul 2006 sponsored by ISTE and organized by Department of Electronics andCommunication, Bapuji polytechnic, Davangere.

## **Seminars/Workshops/Events Organized:**

- Organized one week FDP on **Embarking on open source software in an Engineering and Research journey** in association with FOSS group VTU Belagavi from 19<sup>th</sup> to 24<sup>th</sup> April 2022.
- Organized National Level One Week Online faculty Development Program on **“Introduction to Microchip FPGA”** from 19<sup>th</sup> to 23<sup>rd</sup> July 2021 under ECE Research Centre, Department of Electronics & Communication Engineering in association with ISTE, Faculty Chapter BIET, IETE Shivamogga Center & Tecnomix Components Pvt. Ltd., Bengaluru.
- Organized Webinar on **“In-demand technologies, Job opportunities and Skill enhancement”** in association with Abeyaantrix Softlab Pvt. Ltd on 28<sup>th</sup> June 2021.
- Organized webinar on Industrial training/Internship: **“Launchpad for Productive carrier”** in association with Entuple Technologies, Bangalore on 7<sup>th</sup> June 2021.
- Organized Webinar on **“Exposure to Industry 4.0”**, for students to know about the recent trends and also its work and internship opportunities through Herin Electronics, Mysore on 26<sup>th</sup> June 2021.
- Organized **PAPYRUS 2021 -A Virtual Paper Presentation Contest for students** in association with ISTE Students’ Chapter & IETE Students’ Forum, E&CE, BIET on 10<sup>th</sup> June 2021.
- Organized Technical Webinar on **“Machine learning and Deep learning”** on 17<sup>th</sup> June 2021 CEO, Rove Labs Pvt Ltd, Bangalore.
- Organized Webinar on **“Career opportunities after B.Tech”** ECE organized by ACE Engineering Academy on 16<sup>th</sup> June 2021 at 3pm
- Organized webinar on **“Opportunities in Salesforce”** on 31<sup>st</sup> May 2021.
- Organized webinar on **“Industry 4.0”** by CEO, ROVE labs, Mysore on 6<sup>th</sup> Feb 2021`.
- Initiated **Gnana Vrudhi Lecturer Series** by faculty members of the department since Dec 2020.
- Organized Virtual Recruitronics process for final year students by Vikas & Team alumni 2016 batch on 26<sup>th</sup> & 27<sup>th</sup> Sept 2020.
- Motivated to Organize **Online Siddhata-2021** for all students of the department in the month of July 2020.
- Organized Webinar on **“In-demand technologies, Job opportunities and Skill enhancement”** in association with Abeyaantrix Softlab Pvt. Ltd on 28<sup>th</sup> June 2021.
- Organized webinar on Industrial training/Internship: **“Launchpad for Productive carrier”** in association with Entuple Technologies, Bangalore on 7<sup>th</sup> June 2021.
- Organized **Virtual alumni meet of 2019 batch** on 15<sup>th</sup> Aug 2020.
- Organized **Virtual Parents-Teachers meet** for third semester was organized on 15<sup>th</sup>, 16<sup>th</sup>, 18<sup>th</sup> & 21<sup>st</sup> Sept 2020 for a batch of 30 students.
- Organized **Orientation Program for III semester** Students on 17<sup>th</sup> & 18<sup>th</sup> Dec 2020 organized under virtual mode.
- Organized a Value-added training program on **“Object oriented programming using C++”** was organized from 6<sup>th</sup> to 11<sup>th</sup> Nov 2019 for V and VII Sem

students.15 students actively participated. Resource persons were GHLNG, BKL,LR & MKO.

- Organized a One week training program on “**DSP Applications using MATLAB and DSP processor**” for final year students from 21<sup>st</sup> to 25<sup>th</sup> Oct 2019 in association with Abeyaantrix Softlab Pvt. Ltd , Davangere.
- Organized a two days Workshop on “**Mobile Making**” for the Students of VI SEM held on 19<sup>th</sup> and 20<sup>th</sup> Mar 2019. Sri. Shubham Raut, Sr. Design Engineer, ITG Symposium, IIT, Mumbai was the resource person. 50 students actively participated in this workshop. Sri. Kiran Kumar G H., Asst. Professor coordinated the workshop along with HoD.
- Organized one week Faculty Development Programme on “**Applications of LabVIEW in Engineering & Research**” in association VI Solutions, Bengaluru , ISTE Faculty chapter BIET, Davangere, IETE Shivamogga Centre and IE, Davangere Local centre from 28<sup>th</sup> Jan to 2<sup>nd</sup> Feb 2019.
- Organized one week **Lecture Series-2018** by alumni of our department under EC Forum from 29<sup>th</sup> Sept to 5<sup>th</sup> Oct 2018.
- Organized Two Day Workshop for Faculty on “**ARM Cortex M3 Microcontroller**” in association with Advanced Electronic Systems(ALS), Bengaluru and ISTE Faculty chapter BIET, Davangere, on 23<sup>rd</sup> & 24<sup>th</sup> Jan 2018.
- Organized a **National Conference on “Recent Advances in Electronics and Communication Engineering” – NCRAECE-17** on 13<sup>th</sup> October 2017 in association with Indian Society for Technical Education, New Delhi, ISTE-BIET Faculty chapter and Institution of Electronics & Telecommunication Engineers, Shimoga Center and inauguration of **Professional Activity Center PAC** by chairman Prof H K Harish, IETE Shimoga Center.
- Organized a Two day work shop on “ **System Design using vivado Design suite and Zynq-7000 SoC** ” in association CoreEL Technologies and ISTE Faculty chapter BIET, Davangere on 15<sup>th</sup> & 16<sup>th</sup> Jun 2017.
- Organized an **Alumni Meet** for EC students **MILANA 2017** on 8<sup>th</sup> Apr 2017.
- **Initiated** the State Level Technical Paper Presentation “**Papyrus**” under the banner of E&CE Forum, “**LaTex-A Computerized Typesetting Program**” for final year students,
- Organized five day workshop on “**Labview**” in association with VI Solutions, Bangalore for pre-final year students in 2015 & 2016.
- Organized a two day National Level “**PCB Designing**” workshop cum championship in association with MAGNIVA Technologies Pvt. Ltd., MEGALITH-15, IIT, Kharagpur for IV sem students for the year 2015 & 2016.
- Arranged a five day Value Added Training Programme on “**Arduino & LabVIEW**”, for second year students at BIET, from 25<sup>th</sup> to 29<sup>th</sup> Jul 2016.
- Organized Two Day Faculty Development program on “**Image processing & its Applications**” in association with IETE Shimoga Sub-Center, IETE, New Delhi & ISTE Karnataka Section & ISTE Faculty chapter BIET, Davangere, on 29<sup>th</sup> & 30<sup>th</sup> Oct 2014.
- Arranged an interacting session for E&CE faculties with Dr. Uday Kumar,Raman Research Institute, Bangalore from on 31<sup>st</sup> Oct 2014.
- Arranged a Microsoft Technologies India Ltd. hosted webinar session for female students of E&CE, CSE and ISE all semester on "**WOMEN IN TECH**". The same was conducted by Mr. Lohith G K and team on 31<sup>st</sup> Oct 2014.
- Organized a two day workshop on "**Overview of VLSI and Cadence's Skill Automation**" for final year students on 26<sup>th</sup> & 27<sup>th</sup> Apr 2014.

- Organized a two day workshop "**ROBODUINO**" for pre-final year students. The same was conducted by a team of 10 final year students of Dept. of E&CE on 22<sup>nd</sup> and 23<sup>rd</sup> Sep 2014.
- Organized a three day workshop "**Trace the Copper-An Art of PCB Making**" for pre-final year students. The same was conducted by a team of 13 final year students of Dept. of E&CE on 3<sup>rd</sup> to 5<sup>th</sup> Sep 2014.
- Organized a technical talk on "**Recent Trends in Wireless Communication**" for students and faculties of Dept. of E&CE addressed by Mr. Richard Paul, Senior Technical Specialist, Alcatel Lucent, Bangalore on 19<sup>th</sup> Aug 2014.
- Organized a seminar on "**Introduction to Workshop on Embedded Systems**" by MaMinno Technologies for pre-final year and final year students of Dept. of E&CE at Aryabhata Seminar hall on 28<sup>th</sup> Mar 2014.
- Organized a technical talk on "**Digital EPABX/Intercom System**" resourced by Mr. Prasad Matt, Creative Communications, Davangere and Mr. Siddaraju from Compac TeleSystems, Bangalore on 28<sup>th</sup> Jan 2014.
- Organized a two day workshop on "**Cadence Tool**" for faculties of E&CE, E&I and BME, resourced by Mr. Vijay Kumar P S, Cadence Design Systems India Pvt. Ltd., Bangalore on 24<sup>th</sup> & 25<sup>th</sup> Oct 2013.
- Monitored two day technical event "**Funtronics**" organized by Department of E&CE in association with ISTE student's chapter on 10<sup>th</sup> & 11<sup>th</sup> Oct 2013.
- Organized seminar on "**Recent Trends in Optical Fiber Communication and Networks**" for pre-final year and final year students by Dr. P C Srikanth, HOD, Malnad College of Engineering, Hassan on 5<sup>th</sup> Sep 2013.
- Organized Two day workshop on "**PCB design and Application of Microcontroller**" for students from 20<sup>th</sup> to 22<sup>nd</sup> Sep 2013.
- Organized two days training program on "**Robotics and AVR Microcontroller**" for pre-final year students of BIET, JIT and UBDTCE, Davangere in association with Pravega, IISC-Bangalore and UV Soft Technologies Pvt. Ltd., Delhi, on 28<sup>th</sup> and 29<sup>th</sup> Sep 2013.
- Organized Two day workshop on "**PCB Etching and Multi-utility Microcontroller Board Design**" for students on 23<sup>rd</sup> and 24<sup>th</sup> Mar 2013.
- Organized a seminar on "Image Processing and its applications" for faculty and students on 27<sup>th</sup> Apr 2012.
- Organised a Two days work shop on "**LABView and its Applications**" organized by BIET Davangere and Trident Technologies Bangalore on 2<sup>nd</sup> & 3<sup>rd</sup> August 2012.
- Co-ordinator for "**Low power embedded system using Texas Instrument MSP 430**" organized by E&CE, BIET in association with TI University program and Cranes Software Intl. Ltd on 9<sup>th</sup> & 10<sup>th</sup> Feb 2012.
- Organized a Workshop on "**Digital System Design using HDL – Synthesis & Simulation using Test Benches**" from 16<sup>th</sup> to 18<sup>th</sup> Apr 2009, at BIET, Davangere.
- Worked as Coordinator for the Certification course on "**Embedded Systems & Design**" organized by B.I.E.T and C.I.T.L. Bangalore, under Embedded Techpark from 8<sup>th</sup> Apr to 23<sup>rd</sup> May 2004 (Two weeks).

- Worked as Coordinator for the Certification course on “**VHDL Coding for Synthesis**”, organized by SIMS, Bangalore and E&C Dept B.I.E.T, Davangere from 4<sup>th</sup> to 19<sup>th</sup> Feb 2000.

## **Seminars/Workshops Attended:**

### **Workshop:**

- Participated in one week FDP on **Embarking on open source software in an Engineering and Research journey** in association with FOSS group VTU Belagavi from 19<sup>th</sup> to 24<sup>th</sup> April 2022.
- Dr. G S Sunitha has attended **An Overview of Teaching Techniques in Basic Electronics and Communication Engineering** organized by AICTE-VTU, Muddenahalli, from 13<sup>th</sup> to 17<sup>th</sup> December 2021.
- Dr. G.S. Sunitha has attended Two Day Workshop on **Implementation of National Education Policy-2020**, organized by BIET Davangere on 10th and 11th December 2021.
- Participated in National Level One Week Online faculty Development Program on **Introduction to Microchip FPGA** from 19th to 23rd July 2021 under ECE Research Centre, Department of Electronics & Communication Engineering in association with ISTE, Faculty Chapter BIET, IETE Shivamogga Center & Tecnomic Components Pvt. Ltd., Bengaluru.
- Attended AICTE-ISTE approved Orientation/ Refresher Programme on Accreditation: **An Accelerator for Quality Improvement in Engineering Education** from 18<sup>th</sup> to 24<sup>th</sup> February 2021, organized by SDM college of Engineering, Dharwad
- Participated in Five days Faculty Development Programme (online) on **Signal Processing: Opportunities and Trends 2020** held at Department of Electronics and Communication Engineering, S.D.M. College of Engineering and Technology, Dharwad from 04<sup>th</sup> Aug 2020 to 08<sup>th</sup> Aug 2020.
- Successfully Completed Online AICTE- ISTE approved Orientation / Refresher Programme on **Accreditation: An Accelerator for Quality Improvement in Engineering Education** organized by S D M College of Engineering and Technology, Dharwad, Karnataka from 18<sup>th</sup> Feb to 24<sup>th</sup> Feb 2021.
- Successfully Completed 6 Days online AICTE – ISTE FUNDED INDUCTION / REFRESHER PROGRAM on **Future Wireless Communication: Standards & Technologies** organized by K.S. Institute of Technology, Bangalore from 24<sup>th</sup> to 29<sup>th</sup> May 2021.
- Attended One day workshop on **Intellectual Property Rights** organized with Karnataka State Council for Science & Technology (KSCST), Indian Institute of Science campus, Bangalore via Virtual meeting on 22<sup>nd</sup> Sept 2020.
- A “One day workshop on **eSim**, a first course in the IoT series for Teachers” held at BIET in IS & E Dept. on 21<sup>st</sup> Sep 2019 organised by the by the Teaching Learning Centre(ICT) at IIT Bombay, funded by the Pandit Madan Malaviya National Mission on Teachers and Training (PMMMNTT),MHRD, Govt.of India.
- One week Faculty Development Programme on “**Applications of LabVIEW in Engineering & Research**” in association VI Solutions, Bengaluru , ISTE Faculty chapter BIET, Davangere, IETE Shivamogga Centre and IE, Davangere Local centre from 28<sup>th</sup> Jan to 2<sup>nd</sup> Feb 2019.



- As BOE Representative attended one day workshop on **AICTE Model Curriculum** for First Year BE in Sahayadhri College of Engineering and Management, Mangalore on 19<sup>th</sup> May 2018.
- Two Day Workshop for Faculty on “**ARM Cortex M3 Microcontroller**” in association with Advanced Electronic Systems (ALS), Bengaluru and ISTE Faculty chapter BIET, Davangere, on 23<sup>rd</sup> & 24<sup>th</sup> Jan 2018.
- Two day work shop on “**System Design using vivado Design suite and Zynq-7000 SoC**” in association CoreEL Technologies and ISTE Faculty chapter BIET, Davangere on 15<sup>th</sup> & 16<sup>th</sup> Jun 2017.
- One day Workshop on “**Intellectual Property Rights**” organized by Research & Development Cell, BIET, Davangere on 6<sup>th</sup> Apr 2017.
- One day “Workshop on **Research Methodology**” organized by Research & Development Cell in association with ISTE-BIET Chapter, BIET on 28<sup>th</sup> Oct 2015.
- Two days workshop on “**Women Empowerment**” held on 18<sup>th</sup> & 19<sup>th</sup> Dec 2014 organized by IS & E dept. in association with WPLP programme of ISTE, New Delhi.
- Two Day Faculty Development program on “**Image processing & its Applications**” in association with IETE Shimoga Sub-Center, IETE, New Delhi & ISTE Karnataka Section & ISTE Faculty chapter BIET, Davangere, on 29<sup>th</sup> & 30<sup>th</sup> Oct 2014.
- A Two days work shop on “**Xilinx DSP Design Flow Using System Generator**” organized by RV College of Engineering, Bangalore and CoreEL Technologies Bangalore on 23<sup>rd</sup> & 24<sup>th</sup> Aug 2012.
- A Two days work shop on “**LABView and its Applications**” organized by BIET Davangere and Trident Technologies Bangalore on 2<sup>nd</sup> and 3<sup>rd</sup> Aug 2012.
- A work shop on “**Low power embedded system using Texas Instrument MSP 430**” organized by E&CE, BIET in association with TI University program and Cranes Software International Ltd on 9<sup>th</sup> & 10<sup>th</sup> Feb 2012.
- A work shop on “**Low power embedded system-MSP 430**” organized by TI University program in association with Cranes Software International Ltd on 18<sup>th</sup> & 19<sup>th</sup> Jan 2012.
- A workshop on “**Embedded System Design**”, held on 27<sup>th</sup> Apr 2007 organized by Industry Institute Partnership Cell and E&C forum , BIET, Davangere in association with Advanced Electronic Systems , Bangalore.
- A workshop on “**Tanner Tools. Pro- A Complete VLSI Design**”, organized by E&C Dept., ISTE-chapter, BIET, Davangere and Sovereign Technologies, Bangalore on 16<sup>th</sup> Nov.2006.
- A “**Training program on Learning Management Systems**”, co-sponsored by National program on Technology Enhanced Learning (NPTEL) and organized Centre for Electronics Design & Technology (CEDT), Indian Institute of Science, Bangalore, from 2<sup>nd</sup> to 4<sup>th</sup> Mar. 2006.
- A VTU sponsored workshop on “**Three day workshop on DSP**” held from 7<sup>th</sup> Aug to 9<sup>th</sup> Aug 2005 at Bangalore Institute of Technology, Bangalore.
- A course on “**Embedded Systems & Design**” organized by B.I.E.T and C.I.T.L. Bangalore, under Embedded Techpark from 8<sup>th</sup> Apr 2004 to 23<sup>rd</sup> May 2004 (two weeks).
- A workshop on “**Processor Architecture & Embedded Control System Design**” organized by SDMCE, Dharwad, held on 18<sup>th</sup> May 2002.
- A “**Certification course on VLSI & VHDL**”, organized by SIMS, Bangalore and E&C Dept B.I.E.T, Davangere from 10<sup>th</sup> Feb to 21<sup>st</sup> Mar 2001.

### Short Term Training Programme:

- One week Faculty Development Programme on “ Applications of LabVIEW in Engineering and Research ” in association VI Solutions, Bengaluru , ISTE Faculty chapter BIET, Davangere, IETE Shivamogga Centre and IE, Davangere Local centre from 28<sup>th</sup> Jan to 2<sup>nd</sup> Feb 2019.
- Participated and successfully completed five days short term course on “**Microcontroller Applications**” under Quality Improvement Programme of All India Council for Technnical Education, New Delhi from 11<sup>th</sup> to 15<sup>th</sup> Jul 2016 at Indian Institute of Science, Bangalore.
- Participated and successfully completed four days workshop on “**Training of Evaluators on Outcome Based Education and Accreditation**” jointly organized by NBA, New Delhi and Bapuji Institute of Engineering and Technology , Davangere phase I webinar on 5<sup>th</sup> March and phase II from 21<sup>st</sup> to 23<sup>rd</sup> Mar 2014.
- A Short Term Training Programme on “**Train the Trainer program on soft skills**” from 19<sup>th</sup> Mar to 22<sup>nd</sup> Mar 2012, Conducted by Infosys Limited, Mysore.
- A Five-day workshop on “**Network Management & Network Simulation using NS2**” held from 16<sup>th</sup> to 20<sup>th</sup> Aug 2005 at NMAM Institute of Technology, Nitte.
- A workshop on “**Quality of Service on Internet**” held from 4<sup>th</sup> Jul to 9<sup>th</sup> Jul 2005 organised by Bapuji Institute of Engineering & Technology in collaboration with Council of Scientific and Industrial Research, New Delhi & VTU, Belagum.
- A Short Term Training Programme on “**Trends in Broad BandCommunication**” from 30<sup>th</sup> Aug to 4<sup>th</sup> Sept 2004, sponsored by ISTE at R.V.College of Engineering, Bangalore.
- AICTE-ISTE Two week Short Term Training Programme on “**Recent Advances in Biomedical Engineering**” from 29<sup>th</sup> Jul to 09<sup>th</sup> Aug 2002 organized by department of Mechanical Engineering and Instrumentation Technology at Bapuji Institute of Engineering & Technology, Davangere.
- A MHRD sponsored Short Term course under the joint Telematics program on “**Applications of DSP in wireless Communications and Image Processing**” organized by Electrical Engineering department from 11<sup>th</sup> to 21<sup>th</sup> Dec 2001 at Indian Institute of Technology, Madras.
- A National Level workshop on “**VLSI & CAD for VLSI**” organized by Industrial Consultancy & Research Center of BIT Bangalore, from 4<sup>th</sup> to 9<sup>th</sup> Sept. 2000.

### Seminar:

- Participated in Interactive session with Dr. R C Hiremath, former Principal, KLE, Bangalore on **NAAC Prospectus, Procedures and Process** in association with IE, Local Center and ISTE Students Chapter on 22<sup>nd</sup> July 2021.
- Attended a seminar on **New Education Policy- Concepts & Challenges** by Dr. Subramanya, Prinipal, RV college of Engineering Banglore organized by BIET Davanagere on 13<sup>th</sup> March 2021
- Attended a seminar on **Role of Placement Coordniators for Enhancing Campus Placements-** by Dr. D Ranganath, Dean placement RV college

Bangalore, organized by BIET, Davangere on 9<sup>th</sup> Jan 2021.

- A Seminar on “Markov Chains and its Applications” held on 22<sup>nd</sup> April 2016, organized by the department of Master of Computer Applications, Bapuji Institute of Engineering and Technology, Davangere.
- A Seminar on “Optical Networks” held on 10<sup>th</sup> Mar 2007, sponsored by TEQIP and organized by the department of studies in Electronics and communication Engineering at University B D T College of Engineering, Davangere.
- A National seminar on “Educational Scenario of the 21<sup>st</sup> Century” held on 03<sup>rd</sup> Feb 2007, organized by A.V. Kamamma College for Women, Davangere.

### Webinar:

- Attended Webinar on **RF Engineering and Its Application** organized by IEEE KSSEM Student Branch in association with IEEE Bangalore Section on 16<sup>th</sup> Apr, 2021.
- Attended Webinar on **International Workshop on Wireless Modules** organized by IEEE Atria Student Branch in association with IEEE Bangalore Section on 20<sup>th</sup> Mar, 2021.
- Attended a Webinar on **Assistive Technology for Empowerment of Persons with Disability**, organized by BIET and Composite Regional Centre (CRC) on 11<sup>th</sup> Dec 2020
- Attended a Webinar on **Remote Lab Platform For Hardware Circuit Experiments** organized by *Dept.of E&CE,BIET in association with Institution of Engineers(India), Davangere Local center,Davangere.* on 10<sup>th</sup> Nov 2020.

### Publications:

- Shreevathsa N.S, Shravya N, Veeresh Banakar, Dr. G.S. Sunitha Design and Verification of CMOS LDO Regulator, **2nd International Conference on Emerging Trends in Science, Engineering and Management – 2021**, 15th and 16th July 2021 organized by GM Institute of Technology, Davangere, Karnataka, India through Virtual conference and got Best Research Paper award.
- Rakesh H.M, Dr. G.S. Sunitha, Design and Implementation of Novel 32-Bit MAC Unit for DSP Applications, **2020 International Conference for Emerging Technology (INCET)**, pp.1-6, 5th June 2020.
- Rakesh H.M, Dr. G.S. Sunitha, “A Survey on Architectures of MAC units for DSP Applications”, European Journal of Advances in Engineering and Technology (EJAET), ISSN: 2394 - 658X, 6(4) pp.36-39 , 2019
- Prakash K M, Sunitha G S, " A Modified-SCD for Optimize Performance Rate at Polar Code” International Journal of Green Computing, Vol. 10 Issue 1, pp. 1-21, 2019. DOI: 10.4018/IJGC.2019010101
- Prakash K M , Sunitha G S, "Efficient High performance successive cancellation decoder for polar code” 2018 3rd IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT-2018),held on 18th & 19th May 2018 ,organized by Department of E&CE, Sri Venkateshwara

College of Engineering, Bengaluru. ISBN: 978-1-5386-2440-1

- Rakesh H.M, Dr. G.S. Sunitha, “Performance Comparison of Conventional multiplier with Vedic Multiplier using ISE simulator”, International Journal of Engineering and Manufacturing Science, ISSN 2249-3115 Volume 8, Number 1 (2018) pp. 95-103. <https://www.ripublication.com/Volume/ijemsv8n1spl.htm>
- Rakesh H.M, Dr. G.S. Sunitha, “Comparative analysis of 16 x 16 bit Vedic and Booth multipliers”, World Journal of Technology, Research and Engineering (WJTER), Volume 3, Issue 1, pp.305-313, Jan 2018.
- Rakesh H.M, Dr. G.S. Sunitha, “Design and Implementation of Vedic and Booth Multiplier on Spartan 3”, International Journal of VLSI Design, Microelectronics and Embedded System, Volume 2, Issue 2 ,pp.34-40, Dec 2017.
- Rakesh H.M, Dr. G.S. Sunitha, “Performance comparison of conventional multiplier with Vedic multiplier using ISE simulator”, National Conference on Recent Advances in Electronics and Communication Engineering NCRAECE, Davangere, Oct 2017.
- Prakash K M ,Sunitha G S, “Non-recursive complexity reduction encoding scheme for performance enhancement of polar codes” International Journal of Engineering and Technology (IJET), ISSN (Print): 2319-8613 ISSN (Online) : 0975-4024 vol 9, Issue 1 2017 pp 1-7.
- Prakash K M, Sunitha G S, "Complexity reduction scheme in encoding and decoding for Polar Codes” 2016 IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT-2016),IEEE Catalog Number: CFP16F77-ART (Xplore) ISBN: 978-1-5090-0774-5 (Xplore)
- Sunitha G. S, Suresh Chandra Mohan, “Performance Metrics of ATM & IP Network for Multiple Profiles with 99%Background Utilization of Link” International Journal of Electronics Communication and Computer Engineering (IJECCCE), ISSN (Online): 2249– 071X, ISSN (Print): 2278–4209 Vol 4, Issue 2, March-April 2013, pp 654-662.
- Sunitha G S, Suresh Chandra Mohan, “Performance Comparison of ATM and IP Network for Multiple Profiles using OPNET Simulator”, International Journal on Communications Antenna and Propagation (IRECAP), ISSN 2039-5086 Vol 1, No.5, Oct 2011,pp 471-477, Italy.
- Sunitha G S, Suresh Chandra Mohan, “Performance Comparison of CODEC for ATM Networks using OPNET”, Proceedings National Conference on Electronics, Communications and Computers (NCECC 2009), IETE Navi Mumbai sub centre, pp. 59- 61, Feb 2009.
- Sunitha G S, Suresh Chandra Mohan, “A study of Quality of Service in high speed ATM Networks”, Proceedings of the National Conference on Computer Networks(NCCN 2009), Reva Institute of Technology & Management, Bangalore, pp. 105-107, Feb 2009.
- A course material titled “Digital Telephony” has been submitted to Center for National Academic Network for Continuing Education, AICTE, New Delhi,

during the academic year 2000-2001.

- A course material titled “Error Control Coding” has been submitted to Center for

National Academic Network for Continuing Education, AICTE, New Delhi, during the academic year 1996-1997.

### Proposals submitted to AICTE for STTP/ FDP

Sl.No	Type	Technical Field of Proposal	Title of Proposal	Application Id / Year
1	FDP	Signal Processing and Image Processing	Signal Processing and Image Processing using LabVIEW	1-2006712201 / 2013-2014
2	FDP AICTE-ISTE	Digital Signal Processing	Digital Signal Processing & its Applications	2014-2015
3	STTP	Advanced VLSI Design and Verification	Training in system Verilog for Functional Verification of ASIC's	1-4144138280 / 2018-2019
4	STTP	Measurement and Automation using Virtual Instrument.	Trans disciplinary Approach Using Graphical Environment in Engineering Education	1-7055250981 2019-2020
5	AICTE MODROBS	AI and ML	Modernization of Microprocessor and Computer Lab with Artificial Intelligence (AI) and Machine Learning (ML) integrated Internet of Things (IoT) Lab	27th January 2021.