

Lingaraju

Assistant Professor

Department of E & C E

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Work Experience: 6.6 Years

- Working as Assistant Professor since Jan 2014 in the department of E & C E (5.6 Months)
- Worked as Assistant Professor since August 2014 in the department of E & E (6 Months)

Educational Qualification:

- M.Tech. [Digital Electronics] from GMIT, Davangere, VTU, 2013, First Class with Distinction.
- B.E [Electronics & Communication] from BGSIT, Bellur Cross, Mandya, VTU, 2011 with First Class. *Dissertation Details:*

BE Project: Digital Image Water Marking

M.Tech Project: “FPGA based exertion of discrete wavelet transform architecture for effectual data sets processing, protracted to three dimensions.” *Core Subjects Taught:*

Electronic Devices, Wireless cellular and LTE 4G, Analog Electronics, Internet of Things, Principles of communication system, Linear Integrated Circuits and Its Applications, Digital Switching Systems, Operating System, Management and Entrepreneurship.

Seminar/ Workshops Attended:

- Attended the two day Workshop on Software Defined Radio on 5th & 6th September 2014 at PESITM shimoga.
- Attended the Two Day Faculty Development Program on System Design Using Vivado Design Suite and Zynq-7000 SoC on 15th & 16th June 2017 at BIET, Davangere.

- Attended the Two Day Workshop on ARM Cortex M3 Microcontroller on 23rd & 24th Jan 2018 at BIET, Davangere.
- Attended the Five Day Workshop on Recent Trends in LTE on 29th Jan to 2nd Feb 2018 at RVCE, Bangalore.
- One day workshop on eSim, a first course in the IoT series for Teachers on 21st Sep 2019.

19• Short term training program through ICT mode, on effective teaching, from 15th July 2019. th to

- Attending 5-day national level online faculty development program on “Artificial Intelligence” organized by Department of CSE, in collaboration with Brain O Vision and National Youth Council of India, from 22-05-2020 to 26-05-2020.
- Participated in Webinar and Quiz on the eve of “World Environment Day-2020” on 05 th June 2020, held at Bapuji Institute of Engineering and Technology, Davangere,Karnataka.

Seminar/ Workshops Conducted/Coordinated:

- Conducted Two day workshop on “*LaTeX – A Computerized Typesetting Program*” for final year students of E&CE at BIET Davanagere on 12th and 13th Feb 2015.
- Conducted One week workshop on “*Arduino & LabView*” on 25th – 29th Jul 2016 BIET,Davangere.
- Conducted one day hands on workshop on “*LaTeX – A Computerized Typesetting Program*” for final year students of E&CE at BIET Davanagere on 1st and 4th October 2018.

Publications:

- Presented a paper and published on college proceedings, entitled “FPGA Based Exertion of Discrete Wavelet Transform Architecture for Effectual Data Sets Processing, Protracted to Three Dimensions “at *SJBIT College Bangalore* on 4th May 2013.
- Presented a paper and published on college proceedings, entitled “FPGA Based Exertion of Discrete Wavelet Transform Architecture for Effectual Data Sets Processing, Protracted to Three Dimensions “at *UBDT College Davangere*.