

***Smt. Savithri G. R.***  
**Assistant Professor**  
**Department of E & C E**  
**BIET, Davanagere**  
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***Work Experience: 18Years***

- Worked as a lecturer in E&CE Department of Vijaya Nagara Engineering College from Oct 2001 to May 2003.
- Worked as a lecturer in E&CE Department of Bapuji Institute of engineering and Technology Davanagere from Aug 2005 to Aug 2012
- Working as Asst. Professor in E&CE Department of Bapuji Institute of engineering and Technology, Davanagere since Sept 2012

***Educational Qualification:***

- Pursuing Ph.D. under the guidance of Dr. G S Sunitha Prof. & Head, Dept. of E & CE, BIET, Davanagere in Networking domain.
- M.Tech. [VLSI Design & Embedded System] from BVB College of Engineering & Technology, Hubli, VTU, 2010.
- B.E [Electronics & Communication] from Vijaya Nagara Engineering College, Bellary, Gulbarga University, 1998.

***Membership:***

- Life Member of Indian Society for Technical Education : LM 53850
- Life Member of Institute of Electronics & Telecommunication Engineers : M219043
- Life Member of Institute of Engineers

***Core Subjects Taught:***

**Digital system Design using Verilog, VLSI Design, Power Electronics, Basic Electronics , Network Analysis, Control System, Switching and Finite Automata Theory, Fundamentals of HDL Electronic Circuits, Solid State Devices & Technology, Optical Fiber Communication, Logic Design, Microwave Engineering, Analog Communication, Analog and Mixed mode VLSI Design, Microwaves and Radar, Linear IC's and Applications, Electronic Instrumentation.**

## *Seminar/Workshops Attended*

- Participated & completed successfully AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "Faculty Development Programme on “VLSI -IP DESIGN Approach to SRAM compiler design” from 16<sup>th</sup> Aug to 20<sup>th</sup> 2021 at GM Institute of Technology.
- Participated & completed successfully AICTE Training and Learning (ATAL) Academy Online FDP on "Artificial Intelligence" from 27<sup>th</sup> Jan to 31<sup>st</sup> Jan 2021 at Sri Jayachamareajendra College of Engineering.
- Participated in the online webinar series on VLSI (ASIC - CONCEPT TO CHIP TAPEOUT) and ELECTRONIC SYSTEM DESIGN SERIES jointly organized with Dept. of ECE, VTU, Belagavi from September 4<sup>th</sup> to 29<sup>th</sup> October 2020.
- Participated in the online FDP “Recent trends in Computer Architecture, VLSI and Embedded Systems” Organized by Department of Electronics and Communication Engineering, GRIET from 8<sup>th</sup> to 13<sup>th</sup> June 2020.
- Attended a one day workshop on “eSim” conducted by IIT Bombay on 21<sup>st</sup> September 2019.
- Attended a six day FDP on “Applications of LabVIEW in Engineering and Research” from 28<sup>th</sup> Jan. to 2<sup>nd</sup> Feb. 2019 conducted by VI Solutions organized by E & CE Dept., BIET, Davanagere.
- Attended a five day workshop on “FUNDAMENTALS OF PHYSICAL DESIGN & VERIFICATION USING CADENCE TOOL FLOW” from 21<sup>st</sup> January to 25<sup>th</sup> January 2019 at Entuple Technologies Private Limited, Bangalore.
- Attended a two day Workshop on “ARM Cortex M3 Microcontroller”, on 23<sup>rd</sup> & 24<sup>th</sup> January 2018 at BIET Davanagere, organized in association with ISTE, BIET chapter and Advanced Electronic systems”, Bangalore.
- Coordinated and attended a Two day workshop on “System Design Using Vivado Design Suite & Zynq-7000 SoC” organized in association with ISTE-BIET Faculty Chapter on 15<sup>th</sup> & 16<sup>th</sup> June 2017.
- Attended a one day Workshop on “Intellectual Property Rights “organized by Research and Development Cell of Bapuji Institute of Engineering and Technology, Davanagere held on 6<sup>th</sup> April 2017.
- Attended a one day Workshop on “Research Methodology”, organized by Research and Development Cell of Bapuji Institute of Engineering and Technology, Davanagere held on 28<sup>th</sup> October 2015.
- Attended a workshop on “VLSI Design Lab using Open Source” conducted by Department of Electronics & Communication and R&D centre, M S Engineering College, Bangalore on 24<sup>th</sup> & 25<sup>th</sup> July 2013.

- **Attended “Feel Teacher” learning and Development Intervention organized by CLHRD, Mangalore conducted from 19<sup>th</sup> April to 21<sup>st</sup> April 2013 at BIET, Davanagere**
- **Attended “MISSION 10X Faculty Empowerment Program” conducted by Wipro, Bangalore at BIET Davanagere on May28th to 1<sup>st</sup> June.**
- **Attended two day faculty development program on “LabVIEW and its Application” on 2<sup>nd</sup> and 3<sup>rd</sup> Aug 2012 organized by department of E&CE in association with ISTE, BIET chapter and Trident Tech Labs Pvt. Ltd. Bangalore, organized.**
- **Attended “MISSION 10X Faculty Empowerment Program” conducted by Wipro, Bangalore at BIET Davanagere on May28th to 1<sup>st</sup> June 2012**
- **Attended one day Workshop on “Embedded Systems Design Principles & Practice”, Faculty Development Workshop, Organized by Board for IT Education Standards (BITES) & BVBCET, Hubli on 15<sup>th</sup> Nov 2010.**
- **Attended Workshop on “Digital System Design using HDL –Synthesis & Simulation using Test Benches” from 16<sup>th</sup> to 18<sup>th</sup> April 2009, at BIET, Davanagere.**
- **Attended a Course on “VLSI Lab using CADENCE Tool” on 25<sup>th</sup> to 27<sup>th</sup> July 2009 organised by E&CE Dept. BVBCET, Hubli.**
- **Attended a Staff Development Program on “ARM Processor Design using Verilog” conducted by E&CE Department, BVBCET, Hubli from 8<sup>th</sup> to 20<sup>th</sup> June 2009.**
- **Attended a Staff Development Program on “CMOS Design and Layout Techniques” at BVBCET E&CE department , Hubli from 4<sup>th</sup> to 9<sup>th</sup> August 2008**
- **Participated in one day workshop on “Embedded System Design” conducted on 27<sup>th</sup> April 2007 at BIET, Davanagere**
- **Participated in the one day workshop on “Tanner tools pro (A complete VLSI design system)” at BIET, Davanagere on 16<sup>th</sup> November 2006.**