Smt. Savithri G. R.
Assistant Professor
Department of E & C E
BIET, Davangere
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Work Experience: 16Years

- **Worked as a lecturer in E&CE Department of Vijaya Nagara Engineering College from Oct 2001 to May 2003.**

- **Worked as a lecturer in E&CE Department of Bapuji Institute of engineering and Technology Davangere from Aug 2005 to Aug 2012**

- **Working as Asst. Professor in E&CE Department of Bapuji Institute of engineering and Technology, Davangere since Sept 2012**

Educational Qualification:

- **M.Tech [VLSI Design & Embedded System] from BVB College of Engineering & Technology, Hubli, VTU, 2010.**

- **B.E [Electronics & Communication] from Vijaya Nagara Engineering College, Bellary, Gulbarga University, 1998.**

Membership:

- **Life Member of Indian Society for Technical Education : LM 53850**
- **Life Member of Institute of Electronics & Telecommunication Engineers : M219043**
- **Life Member of Institute of Engineers**

Core Subjects Taught:
Digital system Design using Verilog, Switching and Finite Automata Theory, Basic Electronics Electronic Circuits, Solid State Devices & Technology, Optical Fibre Communication, Logic Design, Microwave Engineering, Power Electronics, Analog Communication, Analog and Mixed mode VLSI Design, Microwaves and Radar, Network Analysis, Control System, Linear IC's and Applications, Electronic Instrumentation, Fundamentals of HDL.

Seminar/Workshops Attended

- **Attended a one day workshop on "eSim" conducted by IIT Bombay on 21st September 2019.**

- **Attended a six day FDP on "Applications of LabVIEW in Engineering and Research" from 28th Jan. to 2nd Feb. 2019 conducted by VI Solutions organized by E & CE Dept., BIET, Davangere.**

- Attended a five day workshop on "FUNDAMENTALS OF PHYSICAL DESIGN & VERIFICATION USING CADENCE TOOL FLOW" from 21st January to 25th January 2019 at Entuple Technologies Private Limited, Bangalore.

- Attended a two day Workshop on "ARM Cortex M3 Microcontroller", on 23rd & 24th January 2018 at BIET Davangere, organized in association with ISTE, BIET chapter and Advanced Electronic systems", Bangalore.

- Coordinated and attended a Two day workshop on "System Design Using Vivado Design Suite & Zynq-7000 SoC" organized in association with ISTE-BIET Faculty Chapter on 15th & 16th June 2017.

- Attended a one day Workshop on "Intellectual Property Rights "organized by Research and Development Cell of Bapuji Institute of Engineering and Technology, Davangere held on 6th April 2017.

- Attended a one day Workshop on "Research Methodology", organized by Research and Development Cell of Bapuji Institute of Engineering and Technology, Davangere held on 28th October 2015.

- Attended a workshop on "VLSI Design Lab using Open Source" conducted by Department of Electronics & Communication and R&D centre, M S Engineering College, Bangalore on 24th & 25th July 2013.

- Attended "Feel Teacher" learning and Development Intervention organized by CLHRD, Mangalore conducted from 19th April to 21st
April 2013 at BIET, Davangere

- Attended "MISSION 10X Faculty Empowerment Program" conducted by Wipro, Bangalore at BIET Davangere on May28th to
1st June.

- Attended two day faculty development program on "LabVIEW and its Application" on 2nd and 3rd Aug 2012 organized by department of E&CE in association with ISTE, BIET chapter and Trident Tech Labs Pvt. Ltd. Bangalore, organized.

- Attended "MISSION 10X Faculty Empowerment Program" conducted by Wipro, Bangalore at BIET Davangere on May28th to 1st June 2012

- Attended one day Workshop on "Embedded Systems Design Principles & Practice", Faculty Development Workshop, Organized by Board for IT Education Standards (BITES) & BVBCET,Hubli on 15th Nov 2010.

- Attended Workshop on "Digital System Design using HDL –
Synthesis & Simulation using Test Benches" from 16th to 18th April 2009, at BIET, Davangere.

- **Attended a Course on "VLSI Lab using CADENCE Tool" on 25th to 27th July 2009 organised by E&CE Dept. BVBCET, Hubli.**

- **Attended a Staff Development Program on "ARM Processor Design using Verilog" conducted by E&CE Department, BVBCET, Hubli from 8th to 20th June 2009.**

- **Attended a Staff Development Program on "CMOS Design and Layout Techniques" at BVBCET ,E&CE department , Hubli from 4th to 9th August 2008**

- **Participated in one day workshop on "Embedded System Design" conducted on 27th April 2007 at BIET, Davangere**

- **Participated in the one day workshop on "Tanner tolls pro (A complete VLSI design system)" at BIET, Davangere on 16th November 2006.**

Achievements:

- **Registered for Ph.D. in the year 2016**
- **Completed Coursework in the year 2019**