

Mrs. Ramya Annigere
Assistant Professor
Department of E&IE BIET, Davangere
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Work Experience: 3.5 Years

- Worked for Philips Electronics India Ltd. as a consultant software engineer from 1st August 2008 to 25th January 2009.
- Worked as a lecturer in BIET from 1st February 2009 to 30th April 2010.
- Currently Working as Assistant Professor in BIET Davangere from 2nd November 2020.

Educational Qualification:

- M.Tech [Digital Electronics] from G M Institute of Engineering and Technology, Davangere, 2016.
- B.E [Electronics and Instrumentation] from University BDT College of Engineering, Kuvempu University, 2008.

Awards:

- Awarded as University 1st Rank holder of Electronics and Instrumentation Engineering from Kuvempu University, 2008.

Core Subjects Taught:

Digital Logic Design, Lasers and Optical Instrumentation, Electronics Instrumentation and Measurements, Signal Conditioning and Data Acquisition System.

NPTEL Courses:

Registered for 8 weeks course on “System Design using Verilog”

Publications:

1. Mrs. Ramya Annigere, Smt. Savita S Patil, “Convolution and De-convolution using Vedic Mathematics”, International Journal of Advanced Research in Computer and Communication Engineering, Vol. 5, Issue 7, July 2016, page number:595-598.
2. Presented the paper in National Conference on “Recent trends in Communication and Computing” (NCRTECC-16).

FDP:

1. Participated in AICTE Training and Learning (ATAL) Academy Online FDP on "Control Systems & Sensors Technology" from 19-01-2021 to 23-01-2021 organized by Manipal Institute of Technology.
2. Participated in AICTE Training and Learning (ATAL) Academy Online FDP on "Sensors Technology" from 8-2-2021 to 12-02-2021 organized by North-Eastern Hill University.
3. Participated in Online FDP on "Inculcating Universal Human Values in Technological Education" organized by AICTE from 12-07-2021 to 16-07-2021.
4. Participated in National level one week Online FDP on “Introduction to Microchip FPGA” from 19-7-2021 to 23-7-2021 organized by Department of Electronics & Communication Engineering, Bapuji Institute of Engineering & Technology in association with Tecnomics Components Pvt. Ltd. Bengaluru, ISTE Faculty Chapter BIET and IETE Shivamogga Center.
5. Participated in AICTE Training and Learning (ATAL) Academy Online FDP on “Embedded Design and Development using Free / Open Source Tools” from 09-08-2021 to 13-08-2021 organized by Centre for Development of Advanced Computing (CDAC).
6. Participated in AICTE Training and Learning (ATAL) Academy Online FDP on “VLSI -IP Design Approach to SRAM compiler design” from 16-08-2021 to 20-08-2021 organized by GM Institute of Technology.
7. Participated in Online FDP on UHV Refresher 1 Part 1 (An AICTE Initiative on Universal Human Values) organized by AICTE from 30-08-2021 to 03-09-2021.

Webinar:

1. Participated in webinar on “Industry 4.0-Employees Expectation on Fresh Engineering Graduates” conducted by Department of Computer Science & Engineering, BIET, Davangere in association with ICT Academy Bangalore held on 22-05-2021.
2. Participated in webinar on “Blog-a-Thon: Boot Camp and Competition” organized by the Department of Computer Science & Engineering, BIET, Davangere held on 25-03-2021.
3. Participated in NBA Awareness webinar on “Outcome Based Education and Accreditation” jointly organized by National Board of Accreditation, New Delhi and VTU Belgavi for the Engineering colleges and Deemed universities in Karnataka on 16-02-2021.
4. Participated in webinar on “Employability Skill for Women Empowerment in the Changing Environment”, organized by VTU, Women Cell on 5-02-2021.
5. Participated in webinar on “Assistive Technology for Empowerment of Persons with Disability”, conducted on 11th December 2020, organized by the Department of E&CE, BIET, Davangere, in association with Composite Regional Centre (CRC) For Skill Development, Rehabilitation & Empowerment of Persons with Disabilities (Under Administrative Control of National Institute for the Empowerment of Persons with Intellectual Disabilities, Secunderabad), Department of Empowerment of Persons with Disabilities (Divyangjan) & Ministry of Social Justice & Empowerment, Government of India.
6. Attended Lecture Series conducted by R&D cell BIET, Davangere from 19-11-2020 to 26-11-2020.
7. Participated online webinar entitled “Remote Lab Platform for Hardware Circuit Experiments” held on 10/11/2020 organized by the Department of Electronics and Communication Engineering, Bapuji Institute of Engineering and Technology, Davangere, in association with Institution of Engineers (India), Davangere Local center, Davangere.